



09/452 329

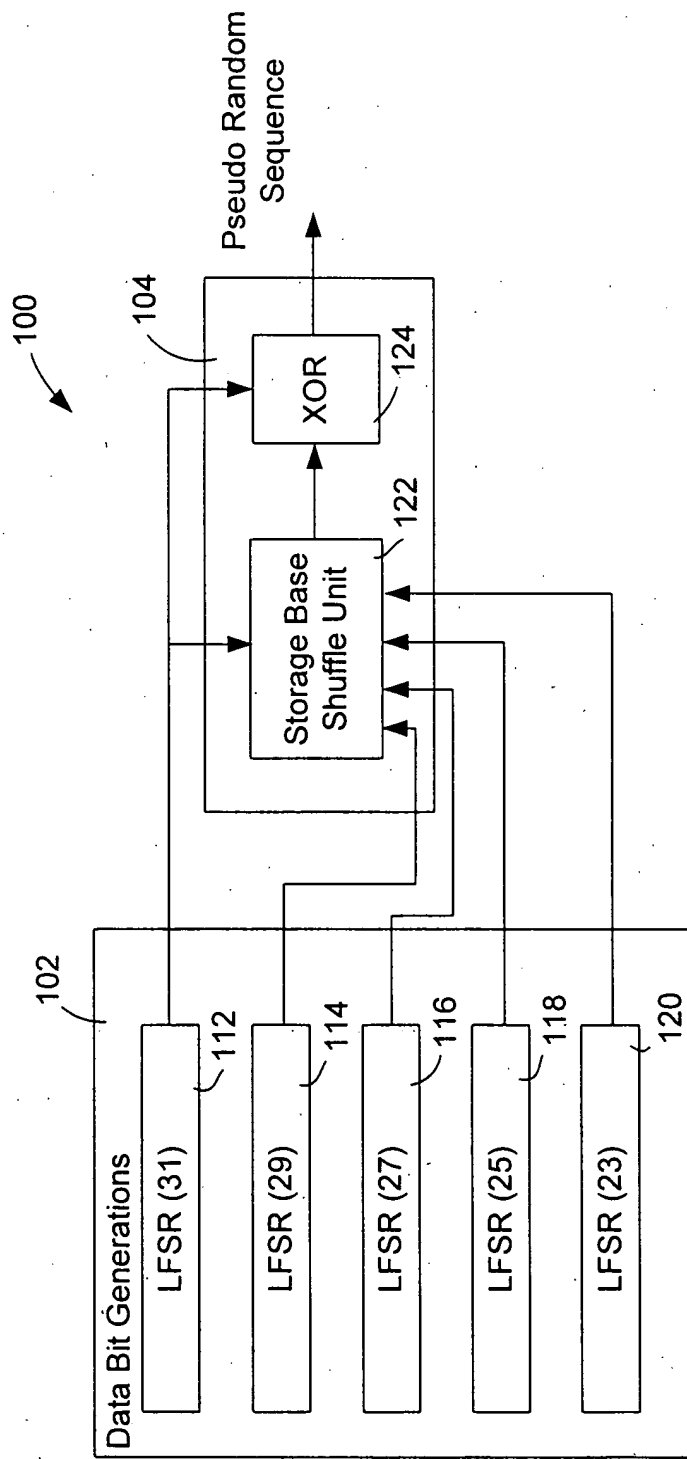
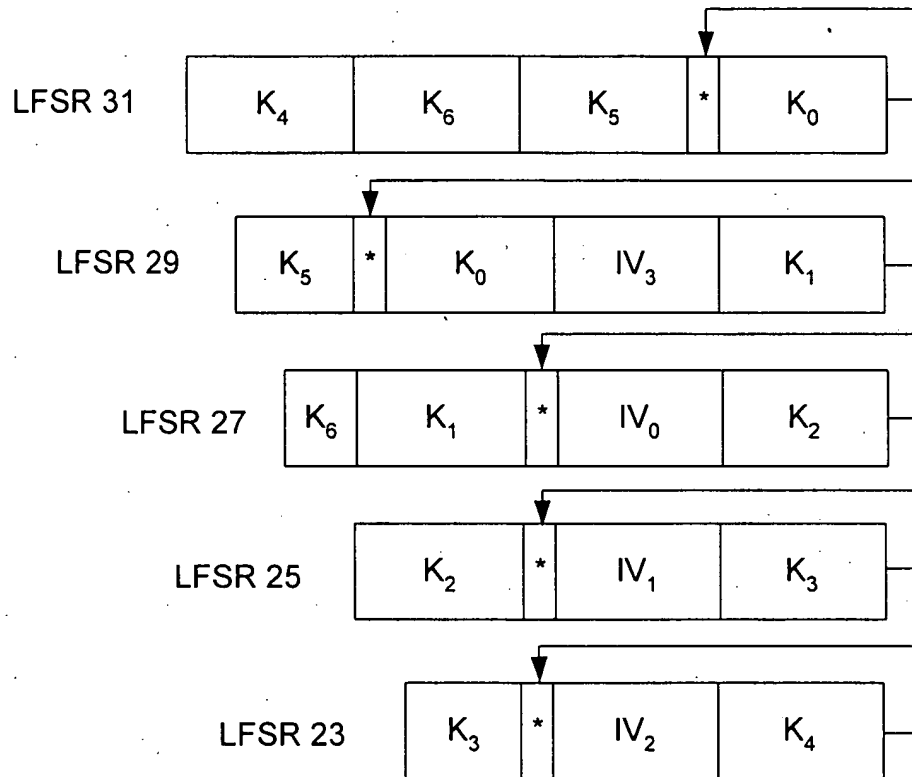


Fig. 1

## Initialization



\* Least significant bit of register is complemented

**Fig. 2**



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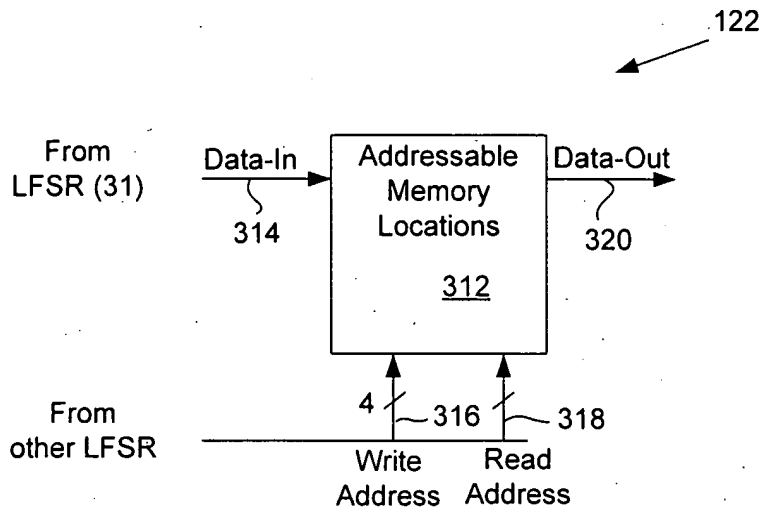


Fig. 3a

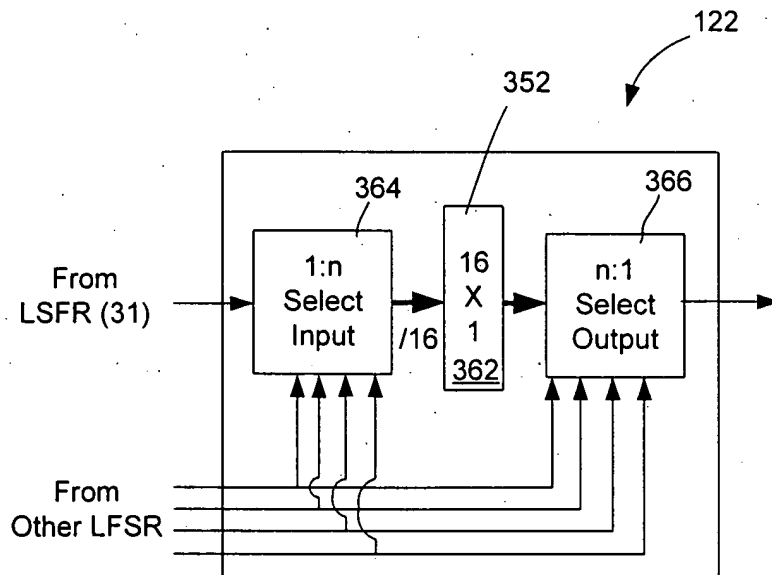


Fig. 3b